

PATENT

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Applicant(s): John S. Yates, Jr., et al.

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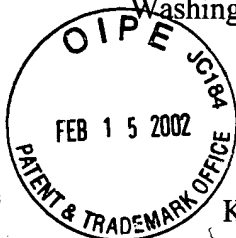
Filed: October 22, 1999

Examiner: unassigned

Title: PROFILING PROGRAM EXECUTION TO IDENTIFY FREQUENTLY-
EXECUTED PORTIONS AND TO ASSIST BINARY TRANSLATION

COMMISSIONER FOR PATENTS

Washington D.C. 20231



PRELIMINARY AMENDMENT AND
TRANSMITTAL OF FORMAL DRAWINGS

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FEB 25 2002

Technology Center 2100

Kindly amend the application as follows.

In the Specification:

Please amend the specification as follows.

At page 9, lines 1-2, change "for translating address references is" to --translates address references--.

At page 32, lines 5-6, change "the I-cache" to --I-cache 112--.

NE At page 38, line 26, replace "the emulator" with --emulator 316--.

At page 38, line 28, replace "The emulator" with --Emulator 316--.

At page 39, line 29, replace "tapestry" by --Tapestry--.

At page 40, lines 11-12, replace "the Tapestry operating system and the X86 operating system" with --Tapestry operating system (312 of Fig. 3a) and X86 operating system 306--.

NE At page 40, line 24, replace "set" with --cleared--.

At page 40, line 28, after the word "If" insert --"a page's"--

At page 42, line 9, replace "the EPC.PC" with --EPC.EIP--

At page 42, line 10, replace "EPC.PC" with --EPC.EIP--

At page 43, line 17, replace "tapestry" with --Tapestry--.

NE At page 50, line 6, replace "EPC" with --EPC.EIP--.

I certify that this correspondence, along with any documents referred to therein, is being deposited with the United States Postal Service on February 8, 2002 as First Class Mail in an envelope with sufficient postage addressed to The Commissioner for Patents, Washington D.C. 20231.

[Signature]